- N.B.: (1) Question No. 1 is compulsory.
 - (2) Attempt any four questions from remaining six questions.
 - (3) Assume suitable data if required and state clearly.
- 1. (a) Draw basic differntiator circuit and derive an expression for output voltage. 5 Explain why this circuit is sensitive to high frequency noise.
 - (b) Explain various electrical parameters of 741C OP-AMP and give the range 5 of values specified in data sheet. 5
 - (c) Draw transfer characteristics of OP-AMP 741C. Explain working of OP-AMP as Noninverting comparator. Draw waveforms.
 - (d) Derive an expression for OP-AMP as an inverting amplifier. 5
- (a) Draw circuit diagram for Instrumentation Amplifier using three OP-AMPs. 10 Derive an expression for output voltage. 5
 - (b) Find v_0 for IOK
 - (c) Draw circuit diagram for peak detector and explain working. 5
- 3. (a) Draw circuit diagram for precision full wave rectifier. Explain working by 10 drawing equivalent circuits for $v_i > 0$ and for $v_i < 0$. Draw necessary waveforms.
 - (b) What are the different types of digital to analogy converters? Explain one of 10 the techniques in detail.
- 4. (a) Draw functional diagram of Astable Multivibrator using timer 555. Derive an 10 expression for time period T.
 - (b) In Astable multivibrator, $R_A = 6.8 k \Omega$, $R_B = 3.3 k \Omega$, $C = 0.1 \mu F$, calculate 10
 - (i) t High

(iii) free running frequency

- (iv) duty cycle.
- (a) Design a low pass, second order KRC filter using equal component design 10 using $f_0 = 1$ KHz and Q = 5. What is its dc gain?
 - (b) Draw functional diagram of PLL 565 and explain its working. 10
- (a) Draw circuit diagram for inverting Schmitt Trigger. Explain working and write 10 an equation for $V_{\rm LTP}$ and $V_{\rm UTP}$.
 - 10 (b) Design phase shift oscillator with $f_0 = 4$ KHz. 20
- 7. Write short notes on any two of the following :-
 - (a) Waveform generator IC 8038
 - (b) Three pin regulators La digital converters