



- (a) Design a linear combination of circuit using Op-amp to combine three sig and give 10 output as, V<sub>0</sub> = -2V<sub>1</sub> 8V<sub>2</sub> V<sub>3</sub>. All resistance values should be ≤ 200 K.
  - (b) In an integrator circuit  $R_1 = 500 \ \Omega$ ,  $R_f = 5 K$ ,  $C = 0.1 \ \mu f$ . If i/p signal period is 10 can work as an integrator. Mark  $f_a$  and  $f_b$ . Also comment whether the circuit can work as an integrator. Mark  $f_a$  and  $f_b$  on integrator frequency response.

## Con. 7830-KR-7259-12.

- State the circuit that can be used for the given application. Draw its circuit diagram and 20
  - (a) A circuit which rectifies sine wave in mv amplitude.
  - (b) A circuit which generates a sine wave signal of fixed frequency.
  - (c) A circuit used to convert any form of the signal into a square wave.
  - (d) A circuit which subtracts two signals.
- Derive max possible efficiency for class B transformer coupled power amplifier at 10 (a)
  - Design a suitable heat sink for the following applications:-Transistor - ZN 3055

Actual power dissipation in transistor = 50 W

Thermal resistance case to heat sink =  $\theta_{C-H} = 0.5$  °C/W.

Ambient temperature = 40 °C

- Write short notes on the following (any two) :-
  - Instrumentation amps.
  - (b) Voltage regulators
  - (c) Op-amp parameters.

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